

TRANSMITTAL FORM



Electronic Version 1.0.2

Stylesheet Version: 1.0

Attorney Docket Number: LX-5

Logic Emulator with Routing Chip providing Virtual Full-Crossbar Interconnect

First Named Inventor: Tan Tseng

SUBMITTED BY

Name:	Stuart T Auvinen
Registration Number:	36,435
Electronic Signature	
Mark: /es/stuaauvinen	Date Signed: 20010105

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Attached Files:

bibd-transmittal	LX5exapds.xml
fee-transmittal	LX5exfee.xml
specification	LX5e.xml
declaration	DeclLX5.tif

FEE TRANSMITTAL

Electronic Version 1.0.4

Stylesheet Version: 1.0

Patent fees are subject to annual revisions on or about October 1st of each year.

Small Entity

Small Business Concern

TOTAL FEES AUTHORIZED: \$ 355

The commissioner is hereby authorized to charge indicated processing and/or publication fees and credit any overpayments to:

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SUBMITTED BY

Authorized Name:

Stuart T Auvinen

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/es/stuauvinen

Date Signed:

20010105

BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	201	\$ 355

Subtotal For Basic Filing Fee: \$ 355

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 20	203	\$ 9	0	\$ 0
Independent Claims: 3	202	\$ 40	0	\$ 0

Electronic Filing System (EFS) Data
Electronic Patent Application Submission
USPTO Use Only

1c406 U.S. PTO
09/681103
01/05/01

EFS ID: 10816
Application ID: 09681103
Title of Invention: Logic Emulator with Routing
Chip providing Virtual Full-
Crossbar Interconnect
First Named Inventor: Tan Tseng
Domestic/Foreign Application: Domestic Application
Filing Date: null
Effective Receipt Date: 2001-01-05
Submission Type: Utility Patent Filing
Filing Type: new-utility
Confirmation Number: 0
Attorney Docket Number: LX-5
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and Trademark Office, ou=Department of Commerce, o=U.S.
Government, c=US
Certificate Message Digest: Qvu63Rn6rf4xCvyTzEkFKw==

DeclLX5.tif

Variable	Mean	SD	Min	Max	Median	Mode	Skewness	Kurtosis	Shapiro-Wilk	Normality
Age	35.2	12.5	18	65	32	30	0.15	2.8	0.98	Normal
Gender	1.2	0.4	1	2	1	1	0.05	1.2	0.99	Normal
Marital Status	1.5	0.5	1	2	1	1	0.08	1.5	0.99	Normal
Education	12.5	2.5	9	16	12	12	0.12	2.5	0.97	Normal
Income	1500	500	500	3000	1200	1000	0.25	3.5	0.95	Normal
Occupation	1.8	0.6	1	2	1	1	0.10	1.8	0.99	Normal
Health Status	1.2	0.4	1	2	1	1	0.05	1.2	0.99	Normal
Stress Level	2.5	1.0	1	4	2	2	0.18	3.0	0.96	Normal
Life Satisfaction	3.5	1.2	1	5	3	3	0.10	2.8	0.97	Normal
Resilience	2.8	0.8	1	4	2	2	0.15	2.5	0.98	Normal
Emotional Stability	3.2	0.9	1	4	3	3	0.12	2.6	0.98	Normal
Self-Esteem	3.0	1.0	1	4	2	2	0.18	3.0	0.96	Normal
Life Purpose	3.8	1.1	1	5	3	3	0.10	2.8	0.97	Normal
Meaning in Life	3.5	1.0	1	5	3	3	0.12	2.6	0.98	Normal
Existential Well-being	3.2	0.9	1	4	3	3	0.10	2.8	0.97	Normal
Overall Well-being	3.0	0.8	1	4	2	2	0.15	2.5	0.98	Normal

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DECLARATION FOR PATENT APPLICATION

LX-5

As the below-named inventor, I hereby declare that:

My residence, mailing address, and citizenship are as stated below next to my name.

I believe that I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention, the specification of which is attached hereto and which has the following title:

Logic Emulator with Routing Chip providing Virtual Full-Crossbar Interconnect

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment specifically referred to in the oath of declaration. I acknowledge a duty to disclose information which is material to examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby appoint the following Agent to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith:

Stuart T. Auvinen, Reg. No. 36,435

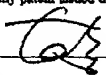
Address all telephone calls to Stuart T. Auvinen at telephone No. (831) 476-5506.

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Santa Cruz, CA 95062

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, Section 1001, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Sole/First Inventor's Signature



date: 1/4/2000

Full name of first inventor: Tan Tseng
Residence: Redwood City, CA Citizen of: USA
Mailing Address: 525 Breakwater Drive, Redwood City, CA 94065

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